

AM01HV12VN1KV1MAP

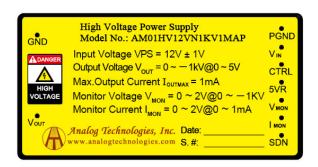




Figure 1. Physical Photos of AM01HV12VN1KV1MAP

FEATURES

Low Power Consumption

High Efficiency

High Stability

Output Current and Voltage Monitors

Small Output Ripple, Time Drift, and Temperature Drift

Overload and Short Circuit Protection

Continuous Linear Adjustment for Output Voltage

Metal Enclosure for Zero EMIS

Easy Control and Installation

Customizable

APPLICATIONS

AM01HV12VN1KV1MAP is a high stability high voltage power supply, ideal for photomultiplier tube, optical measurement, light control technology, detectors. ion beam implantation, capacitor charging, electron beam welding, nuclear physics, withstand voltage test, medical equipment, precision instruments, etc.

DESCRIPTION

AM01HV12VN1KV1MAP is a combination of switching step-up technology and linear regulation, which converts the low input voltage into a stable high output voltage. It comes with output short-circuit protection and a wide range of output voltage adjustments. This high voltage power supply also features ultra-small size, light weight, moisture proof, shockproof, metal enclosure, and zero EMIs. SHUTDOWN

MODE OPERATION

A logic low <0.8V or a 0V on the SDN pin will turn the device off. When SDN is in logic high >1.2V or left unconnected, the product is working well.

SAFETY PRECAUTIONS

The internal protection circuit is provided in the high voltage power supply, but the high voltage short circuit shall be avoided.

Make sure the circuit is insulated perfectly, especially between the high voltage output and the surroundings so as to avoid electronic shock.

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AM01HV12VN1KV1MAP

SPECIFICATIONS

Table 1. Characteristics. $T_A = 25$ °C, unless otherwise noted

Par	ameter	Symbol	Condition	Min.	Тур.	Max.	Unit/Not e
Input	Voltage	V_{IN}		11	12	13	V
Quiescent	Input Current	$I_{\rm INQQ}$	$I_{OUT} = 0mA$	40	50 60		mA
Full Load	Input Current	I_{INFLD}	$I_{OUT} = 2.0 \text{mA}$	200	250 300		mA
Input Voltage	Regulation Ratio	$\Delta V_{OUT}/\Delta V_{IN}$	$V_{IN} = 11V \text{ to } 13V$		0.005		%
Outp	ut Voltage	$V_{ m OUT}$	$I_{OUT} = 0 \text{ to } 1.0 \text{mA}$	0	-1000		V
Maximum	Output Current	I _{OUTMAX}	$V_{IN} = 11V \text{ to } 13V$		1.0		mA
Stability of R	eference Voltage	$V_{ m REF}$	$0 \sim 50^{\circ} \text{C}$	4.95	5 5.05		V
I	Load				1		ΜΩ
Regulation Mode				0 ~ 5V or 10k potentiometer			
Control Input vs. Output Linearity		$\Delta V_{REF}/\Delta V_{OUT}$		Г	<0.1		%
Load Regulation Rate		TELL COL	0 to 1.0mA		≤0.01		%
Output voltage ripple		$ m V_{OUT_RP}$			< 0.001		%V _{P-P}
Monitor Voltage		V _{MON}	$V_{OUT} = 0 \sim -1 \text{kV}$	0		2	V
Monitor Current		I_{MON}	$I_{OUT} = 0 \sim 1.0 \text{mA}$	0		2	V
Instantaneous S	Instantaneous Short Circuit Current				< 500		mA
Shutdown Supply Current		I_{SHDN}				18	mA
Shutdown Lo	Shutdown Logic Input Current					3	uA
Shutdow	Shutdown Logic Low				<0.8		V
Shutdown	Shutdown Logic High				≥1.2		V
Full Loa	Full Load Efficiency				≥80		%
Temperatu	Temperature Coefficient		0 ~ 50°C		< 0.01		%/°C
m: 5-10	Short Time Drift		After 30 min.		< 0.01		%/ h
Time Drift	Long Time Drift		warm-up		< 0.05		%/d
Operating T	Operating Temperature Range			0		50	°C
Storage Tem	Storage Temperature Range			-40		85	°C
External	External Dimensions			45×23×15		mm	
Weight					30		g
					0.07		lbs
					1.06		Oz



TESTING DATA

High voltage power supply testing data (Test condition: the load is 1 M Ω)

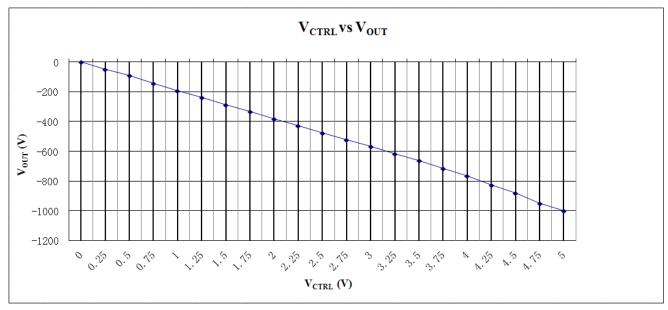
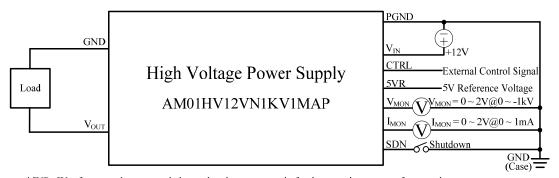


Figure 2. V_{CTRL} vs. V_{OUT}

THE CONNECTION DIAGRAM OF MODULE'S PERIPHERAL CIRCUIT



^{*5}VR: 5V reference voltage can only be used as the power supply for the potentiometer, not for any other parts.

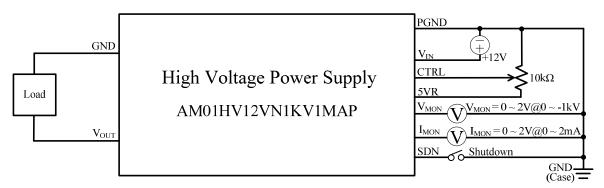
Figure 3. Controlled by External Source

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^{*}SDN: Shutdown Logic Low SDN < 0.8V or 0V on the SDN pin will turn off the high voltage output. Shutdown Logic High SDN > 1.2V or left unconnected will turn on the high voltage output.

^{*}The PGND and GND are connected inside with the case and should be well grounded.

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*5VR: 5V reference voltage can only be used as the power supply for the potentiometer, not for any other parts.

Figure 4. Controlled by Potentiometer

NAMING INSTRUCTIONS

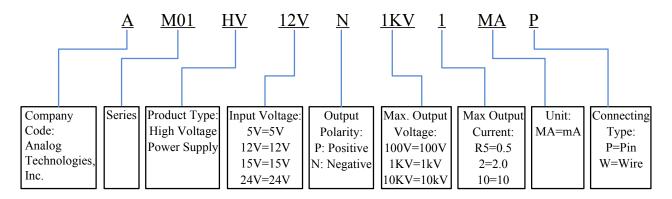


Figure 5. Naming Rules of AM01HV12VN1KV1MAP

DIMENSIONS

I. Pin layout

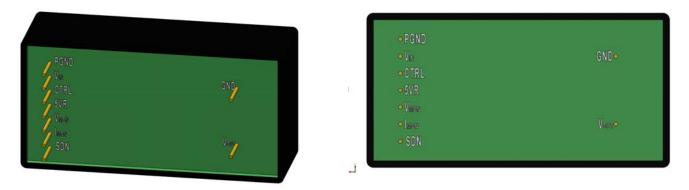


Figure 6. Pin Layout for AM01HV12VN1KV1MAP

^{*}SDN: Shutdown Logic Low SDN < 0.8V or 0V on the SDN pin will turn off the high voltage output.

Shutdown Logic High SDN > 1.2V or left unconnected will turn on the high voltage output.

^{*}The PGND and GND are connected inside with the case and should be well grounded.



II. Dimension of AM01HV12VN1KV1MAP.

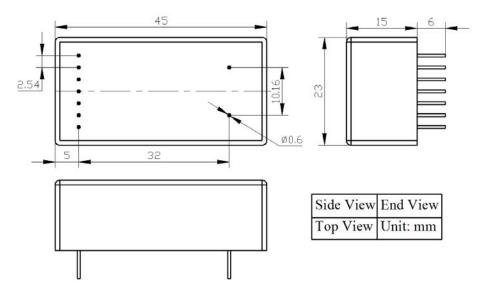


Figure 7. Dimensions for AM01HV12VN1KV1MAP

PRICES

Quantity	1~9pcs	10~49pcs	50~99pcs	≥100pcs	
AM01HV12VN1KV1MAP	\$129	\$119	\$109	\$99	

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High Voltage Power Supply



AM01HV12VN1KV1MAP

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