

APPLICATION NOTE 4141

Programming and Testing a DS2786-Based Circuit

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Abstract: This application note describes how to properly test a circuit board containing a DS2786 open-circuit-voltage battery monitor. A step-by-step test procedure is provided that can be followed to ensure that the circuit board has been assembled properly. Additionally, this note instructs the reader on how to properly program the EEPROM of the DS2786 once it is assembled onto the circuit board.

Introduction

The DS2786 open-circuit-voltage (OCV) based fuel gauge leaves the factory with a default OCV profile and default configuration loaded into EEPROM. In order to increase the accuracy of the OCV fuel gauge and adapt the DS2786 to the specific application, it may be necessary to reprogram the EEPROM of the DS2786. This application note describes how to program the EEPROM and test an assembled circuit board.

Performing a Board-Level Test

The following is an example of how to production test a DS2786-based OCV board before final assembly into a cell pack. Figure 1 shows a sample circuit board schematic utilizing all of the functions of the DS2786. All critical test points (there are 7) are indicated with circled numbers in the figure. The test flow assumes all discrete components of the circuit have been tested, and, therefore, the goal is to verify that the board has been assembled properly by validating the connections.

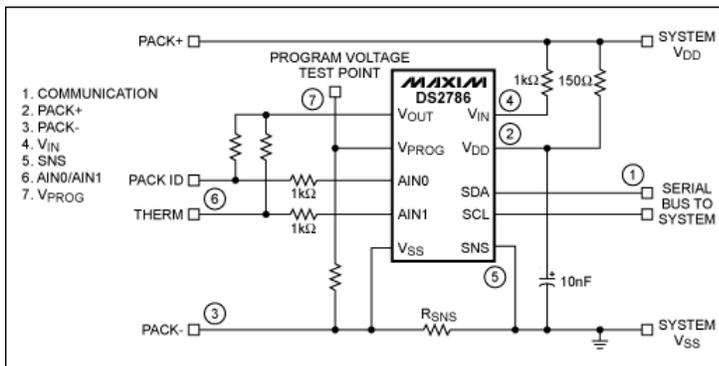


Figure 1. Circuit board nodes that must be verified.

Test 1: Test initialization. The purpose of this test is to determine if there are any direct shorts on the board and verify communication. Successful communication to the device to read the voltage register in this step verifies the SDA and SCL connection (node 1), as well as the Pack+ to the V_{DD} pin (node 2) and the Pack- to the V_{SS} pin (node 3) connections. Additionally, by reading the voltage register in this step and confirming that it is a valid measurement, the connection to the V_{IN} pin (node 4) can be verified.

Force 4.0V from Pack+ to Pack-.

Wait 880ms.

Read the Voltage Register:

Wait for a voltage conversion.

2 Bytes.

Fail board if communication is not possible.

Fail board if voltage reading is inaccurate.

Test 2: Verify SNS (node 5). Connection to the SNS pin can be verified with a valid current measurement.

Force 4.0V from Pack+ to Pack-.

Force 1.0A from Pack- to System V_{SS}.

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