# GCMS040A120B1H1 1200V 40 mohm SiC MOSFET Module



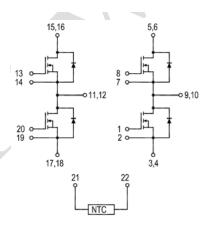
#### Features:

- Ultra Low Loss
- High-Frequency Operation
- Zero Reverse Recovery Current from Diode
- Zero Turn-off Tail Current from MOSFET
- Normally-off, Fail-safe Device Operation
- Easy of Paralleling



- Solar Inverters
- High Voltage DC/DC Converters
- Motor Drives
- EV Chargers
- UPS





### **Absolute Maximum Ratings** (T<sub>C</sub>=25 ℃ unless otherwise specified)

Symbol	Description Value			
V <sub>DSmax</sub>	Drain-Source Voltage	Drain-Source Voltage		V
V <sub>GSmax</sub>	Gate-Source Voltage	Absolute Maximum values	-10/+25	V
V <sub>GSop</sub>	Gate-Source Voltage	Recommended Operational Values	-5/20	V
	Continuous Drain Current	V <sub>GS</sub> =20V,T <sub>C</sub> =25 <sup>0</sup> C	42	Α
I <sub>D(DC)</sub>	Continuous Drain Current	V <sub>GS</sub> =20V,T <sub>C</sub> =100 <sup>0</sup> C	40	Α
I <sub>D(pluse)</sub>	Pulsed Drain Current	Pulse width t <sub>p</sub> limited by T <sub>jmax</sub>	80	Α
P <sub>D</sub>	Power Dissipation	$T_c=25^{\circ}C$ , $T_j=150^{\circ}C$	152	W

Page 1 of 11 Rev. 0.2 4/15/2016



### **Electrical Characteristics of MOSFET** (T<sub>C</sub>=25°C unless otherwise specified)

Symbol	Description	Conditions		Min	Тур	Max	Unit	
V <sub>(BR)DSS</sub>	Drain - Source Breakdown Voltage	V <sub>GS</sub> =0V,I <sub>D</sub> =50uA		1.2			KV	
	-	V <sub>DS</sub> = 10 V, I <sub>D</sub> =1	0 mA, T <sub>j</sub> =25 <sup>0</sup> C		2.9			
$V_{GS(th)(chip)}$	Gate Threshold Voltage	V <sub>DS</sub> = 10 V, I <sub>D</sub> =1		2.4	P	V		
		V <sub>DS</sub> = 10 V, I <sub>D</sub> =1	0 mA, T <sub>j</sub> =150 <sup>0</sup> C		2.3			
		V <sub>DS</sub> = 10 V, I <sub>D</sub> =2	mA, T <sub>j</sub> =25 <sup>0</sup> C	4	3.1	) 7		
$V_{GS(th)\;(terminal)}$	Gate Threshold Voltage	V <sub>DS</sub> = 10 V, I <sub>D</sub> =2	mA, T <sub>j</sub> =125 <sup>0</sup> C		2.6		V	
		V <sub>DS</sub> = 10 V, I <sub>D</sub> =2	mA, T <sub>j</sub> =150 <sup>0</sup> C	7	2.2			
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 1.2 kV, V <sub>GS</sub>	<sub>3</sub> = 0V			1	mA	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = 20 V, V <sub>DS</sub> =	= 0V			200	nA	
D	On Otata Decistance	V <sub>GS</sub> = 20 V, I <sub>DS</sub> = 40 A, T <sub>J</sub> = 25°C V <sub>GS</sub> = 20 V, I <sub>DS</sub> = 40 A, T <sub>J</sub> = 150°C			40		mΩ	
R <sub>DS(on) (chip)</sub>	On State Resistance				84			
		V <sub>GS</sub> = 20 V, I <sub>DS</sub> =	40 A, T <sub>J</sub> = 25°C	45			mΩ	
R <sub>DS(on) (terminal)</sub>	On State Resistance	V <sub>GS</sub> = 20 V, I <sub>DS</sub> = 40 A, T <sub>J</sub> = 125°C V <sub>GS</sub> = 20 V, I <sub>DS</sub> = 40 A, T <sub>J</sub> = 150°C			66			
	6				76			
	Transcenductores	V <sub>DS</sub> = 20 V, I <sub>DS</sub> = 40 A, Tj=25 °C V <sub>DS</sub> = 20 V, I <sub>DS</sub> = 40 A, Tj=150 °C			15.1		c	
9fs	Transconductance				13.2		S	
C <sub>iss(chip)</sub>	Input Capacitance				1893			
C <sub>OSS(chip)</sub>	Output Capacitance	$V_{DS} = 1000V, f = V_{AC} = 25 \text{ mV}$	1MHz,		150		nF	
C <sub>rss(chip)</sub>	Reverse Transfer Capacitance				10			
	7		Tj=25 <sup>0</sup> C		0.70			
E <sub>on</sub>	Turn-On Switching Energy		Tj=125 <sup>0</sup> C		0.76			
		$V_{DD} = 600 \text{ V},$ $V_{GS} = -5\text{V}/+20\text{V}$	Tj=150 <sup>0</sup> C		0.77		1	
E <sub>off</sub>	Turn-Off Switching Energy	$I_D = 40 \text{ A},$ $R_{G(ext)} = 20 \Omega$ $Tj=25^{0}C$ $Tj=125^{0}C$ $Tj=150^{0}C$	Tj=25 <sup>0</sup> C		0.13		mJ	
			Tj=125 <sup>0</sup> C		0.31			
				0.39				
R <sub>G(int)</sub>	Internal Gate Resistance	f =1MHz, V <sub>AC</sub> = 2	5 mV		1.8		Ω	

Page 2 of 11 Rev. 0.2 4/15/2016



Q <sub>GS</sub>	Gate-Source Charge				28	
Q <sub>GD</sub>	Gate-Drain Chrage	$V_{DD}$ = 800 V, $V_{GS}$ = -5V/+20V, $I_{D}$ = 40 A,			37	nC
$Q_{G}$	Total Gate Chrage				115	
			Tj=25 <sup>0</sup> C		66	
$t_{d(on)}$	Turn-on delay time		Tj=125 <sup>0</sup> C		67	
			Tj=150 <sup>0</sup> C		67	
			Tj=25 <sup>0</sup> C		40	
$t_{r}$	Rise Time	V <sub>DD</sub> = 600V,	Tj=125 <sup>0</sup> C	4	40	
		$V_{GS} = -5/+20V$ , $I_{D} = 40 A$ ,	Tj=150 <sup>0</sup> C		38	ns
		$R_{G(ext)}$ = 20 Ω, Timing relative	Tj=25 <sup>0</sup> C		125	
$t_{\text{d(off)}}$	Turn-off delay time	to V <sub>DS</sub>	Tj=125 <sup>0</sup> C		124	
			Tj=150 <sup>0</sup> C		129	
t <sub>f</sub>			Tj=25 <sup>0</sup> C		75	
	Fall Time	AX	Tj=125 <sup>0</sup> C		74	
		Tj=150 <sup>0</sup> C	Tj=150 <sup>0</sup> C		82	
$R_{ heta JCM}$	Thermal Resistance Junction To-Case for MOSFET		-		0.82	°C /W

## Built-in SiC Body Diode Characteristics (T<sub>C</sub>=25<sup>o</sup>C unless otherwise specified)

Symbol	Description	Conditions	Min	Тур	Max	Unit
V 50.1.5	$I_{SD}$ = 20 A, $V_{GS}$ = -5V , $T_j$ =25 $^{0}$ C		3.6		V	
V <sub>SD(chip)</sub>	Diode Forward Voltage	$I_{SD}$ = 20 A, $V_{GS}$ = -5V, $T_j$ =150 $^{0}$ C		3.3		] V
Trr	Reverse Recovery Time	I <sub>SD</sub> = 40 A, V <sub>GS</sub> = -5V ,		54		ns
Qrr	Reverse Recovery Charge	T <sub>j</sub> =25 <sup>0</sup> C,V <sub>R</sub> =800V, di <sub>F</sub> /dt= 1000 A/µs		283		nC
I <sub>rrm</sub>	Peak Reverse Recovery Current	- αι <sub>Ε</sub> /αι- 1000 Α/μδ		15		Α

Page 3 of 11 Rev. 0.2 4/15/2016



### Free-Wheeling SiC Diode Characteristics (T<sub>C</sub>=25 °C unless otherwise specified)

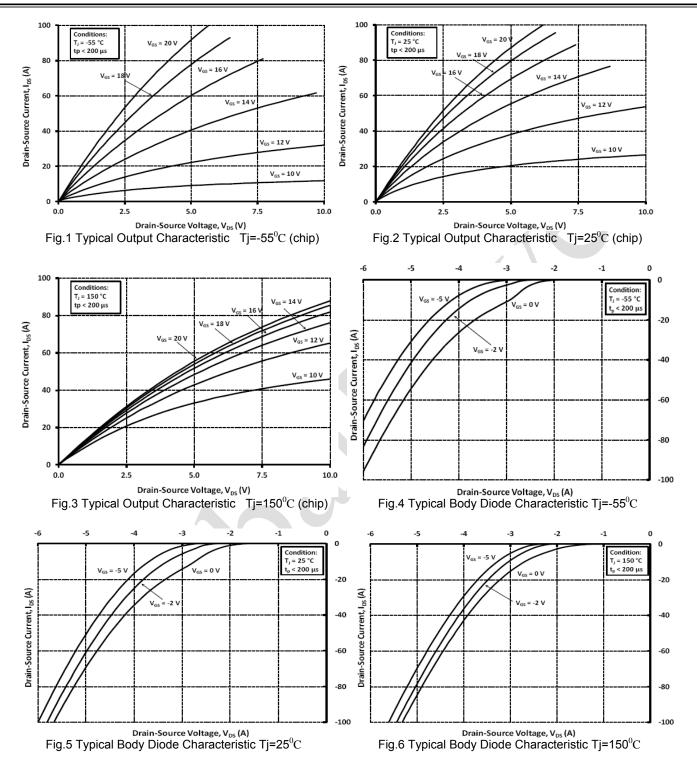
Symbol	Description	Conditions	Min	Тур	Max	Unit
		I <sub>F</sub> = 40 A, V <sub>GS</sub> = 0 V ,T <sub>j</sub> =25 <sup>o</sup> C		1.8		
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 40 A, V <sub>GS</sub> = 0 V ,T <sub>j</sub> =125 <sup>o</sup> C		2.3		V
		$I_F = 40 \text{ A}, V_{GS} = 0 \text{ V}, T_j = 150^{\circ}\text{C}$		2.4	P	
Q <sub>C</sub>	Total Capacitive Charge	V <sub>R</sub> = 1200 V, T <sub>J</sub> = 27°C		129		nC
R <sub>0JC</sub>	Diode Thermal Resistance: Junction-To	o-Case		1.21	) >	<sup>0</sup> C /W
		$T_C=25^{\circ}C$ , $T_j=175^{\circ}C$	, 1	94		
$I_{F(chip)}$	Continuous Diode Forward Current	$T_C=125^{\circ}C$ , $T_j=175^{\circ}C$	7	52		Α
		$T_C=150^{\circ}C, T_j=175^{\circ}C$		35		

### **Module**

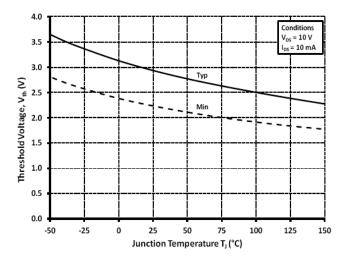
Symbol	Description		Min	Тур	Max	Unit
V <sub>iso</sub>	Isolation Voltage(All Terminals Shorted)	f = 50Hz, 1minute	2500			V
TJ	Maximum Junction Temperature	Maximum Junction Temperature			150	$^{\circ}$
$T_{JOP}$	Maximum Operating Junction Temperature Range		-40		+150	$^{\circ}$ C
T <sub>stg</sub>	Storage Temperature		-40		+125	$^{\circ}$ C
R <sub>ecs</sub>	Case-To-Sink (Conductive Grease Applied)			0.1		°C/W
Т	Mounting Screw:M6		1.0		1.5	N·m
G	Weight			25		g

Page 4 of 11 Rev. 0.2 4/15/2016



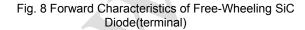


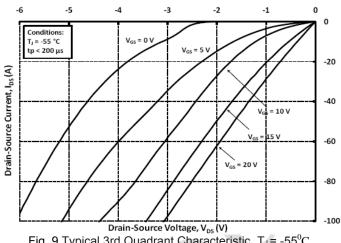




Tյ =25℃ TJ =125°C TJ =150°C 60 50 € ± 40 30 2.6 1.6

Fig.7 Typical and Minimum Threshold Voltage vs. Temperature





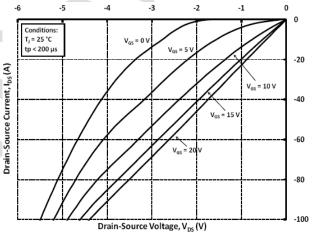
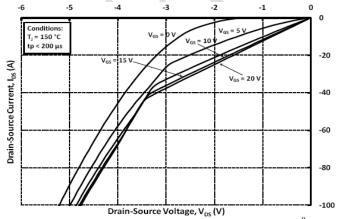


Fig. 9 Typical 3rd Quadrant Characteristic, T<sub>J</sub> = -55<sup>o</sup>C

Fig. 10 Typical 3rd Quadrant Characteristic, T<sub>J</sub> = 25<sup>o</sup>C



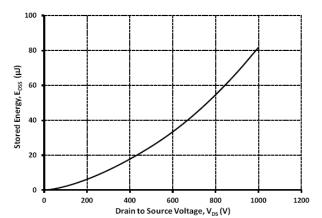
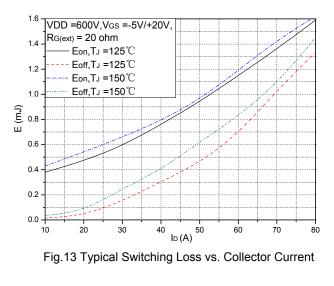


Fig.11 Typical 3rd Quadrant Characteristic, T<sub>J</sub> = 150<sup>o</sup>C

Fig.12 Typical Output Capacitor Stored Energy





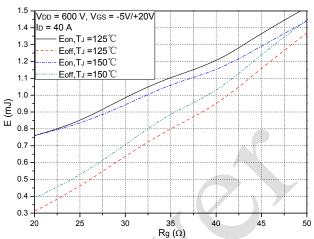


Fig.14 Typical Switching Loss vs. Gate Resistance

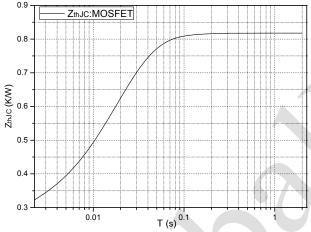


Fig.15 Transient thermal impedance (MOSFET)

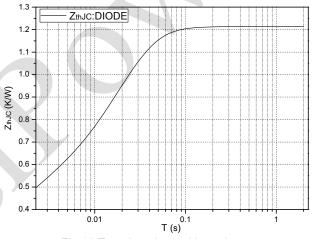


Fig.16 Transient thermal impedance (Free-Wheeling SiC Diode)

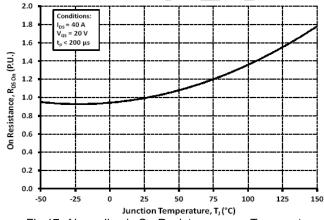


Fig.17 Normalized On-Resistance vs. Temperature

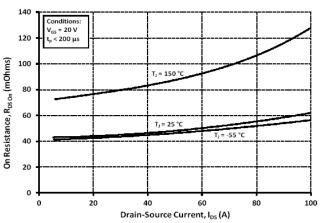


Fig.18 On-Resistance vs. Drain Current For Various Temperatures

Page 7 of 11 Rev. 0.2 4/15/2016



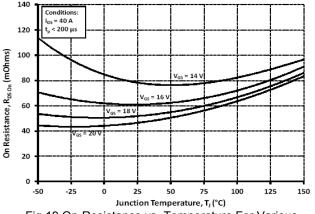


Fig.19 On-Resistance vs. Temperature For Various Gate Voltage

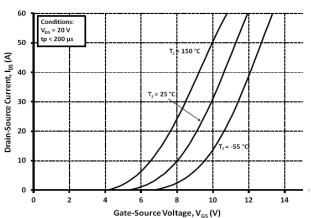


Fig.20 Typical Transfer Characteristic For Various Temperatures

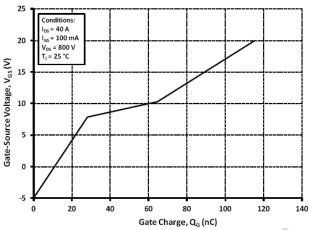


Fig.21 Typical Gate Charge Characteristic 25

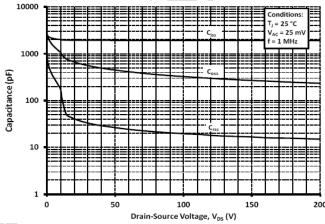


Fig.22 Typical Capacitances vs Drain Voltage (0-200 V)

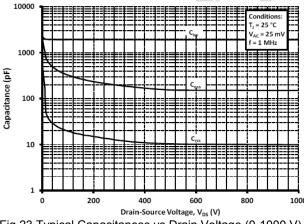


Fig.23 Typical Capacitances vs Drain Voltage (0-1000 V)

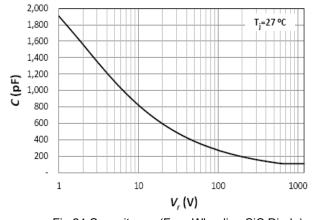
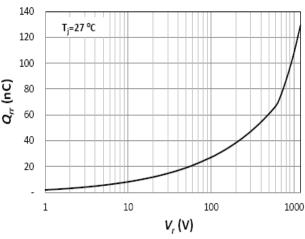


Fig.24 Capacitance (Free-Wheeling SiC Diode)

Page 8 of 11 Rev. 0.2 4/15/2016





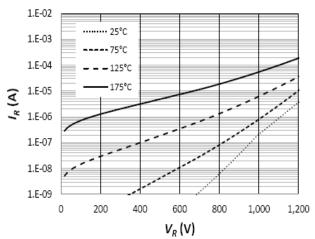
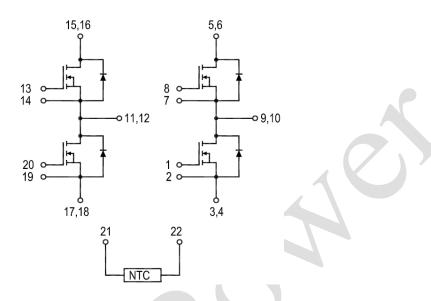


Fig.25 Recovery Charge (Free-Wheeling SiC Diode)

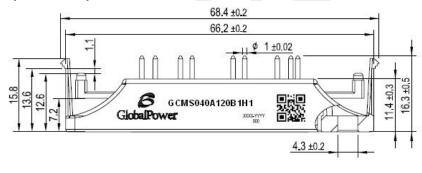
Fig.26 Reverse Characteristics (Free-Wheeling SiC Diode)

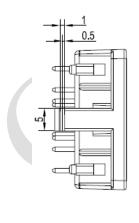
Page 9 of 11 Rev. 0.2 4/15/2016

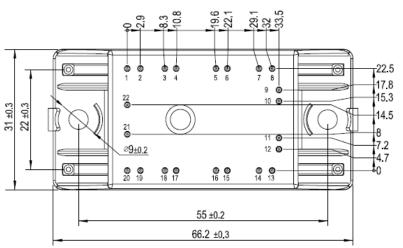
### **Internal Circuit**



### Package Outline (Unit: mm):







Page 10 of 11 Rev. 0.2 4/15/2016



**Revision History** 

Date	Revision	Notes
5/29/2015	0.1	Initial release of preliminary datasheet
4/15/2016	0.2	Add the test data and revised package drawing

#### **Global Power Technologies Group**

20692 Prism Place Lake Forest, CA 92630 TEL (949) 207-7500 FAX (949) 613-7600

E-mail: <u>info@gptechgroup.com</u> Web site: www.gptechgroup.com



#### **Notes**

#### RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented March, 2013. RoHS Declarations for this product can be obtained from the Product Documentation sections of www.gptechgroup.com.

#### REACh Compliance

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact our office at GPTG Headquarters in Lake Forest, California to insure you get the most up-to-date REACh SVHC Declaration.

REACh banned substance information (REACh Article 67) is also available upon request.

- This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control.
- To obtain additional technical information or to place an order for this product, please contact
  us. The information in this datasheet is provided by Global Power Technologies Group.
  GPTG reserves the right to make changes, corrections, modifications, and improvements of
  datasheet without notice.

Page 11 of 11 Rev. 0.2 4/15/2016