



191028660 Datasheet Revision for C8051T620/1/6/7 & C8051T320/1/2/3 to Revision 1.3

Bulletin Issue Date: 10/28/2019

Effective Date: 10/28/2019

Description of Change

Silicon Labs is pleased to announce the release of version 1.3 of the C8051T620/1/6/7 and C8051T320/1/2/3 datasheet.

Reason for Change

Revision 1.2 to Revision 1.3

- Table 2.2 on page 22 added to highlight obsolete OPNs.
- Table 7.2 on page 35:
 - * Updated Supply Voltage footnote.
 - * Removed Supply Voltage, Regulator 1 in Bypass Mode specification.
 - * Removed Supply Voltage, Regulator 1 in Normal Mode test condition.
 - * Renamed Supply Voltage parameter to VDD.
- In "Prefetch Engine" on page 84:
 - * Clarified SYSCLK frequency for using the prefetch engine.
 - * Added recommended PFEN setting for SYSCLK > 25 MHz.
- Removed statement regarding ability to read lock byte regardless of security settings.
- Corrected typographical errors in Table 22.1 on page 140 and Table 22.2 on page 141.

Product Identification

Existing Part #

C8051T320-GQ
C8051T320-GQR
C8051T321-GM
C8051T321-GMR
C8051T322-GQ
C8051T322-GQR
C8051T323-GM
C8051T323-GMR
C8051T620-GM
C8051T620-GMR
C8051T621-GM
C8051T621-GMR
C8051T626-B-GM
C8051T626-B-GMR
C8051T627-B-GM
C8051T627-B-GMR

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

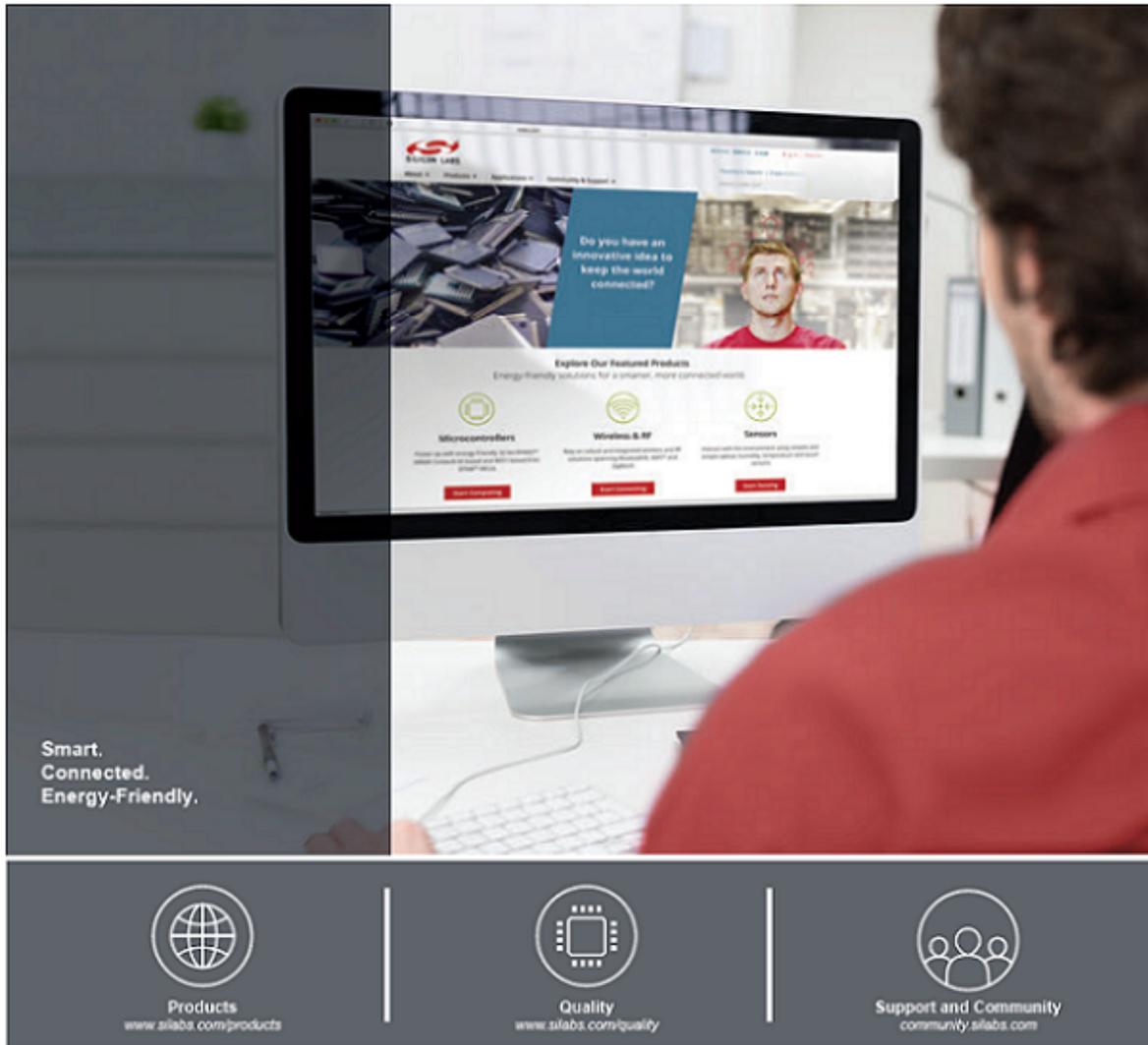
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Customer Actions Needed:

Review datasheet changes. Contact your local Silicon Labs representative with questions.

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